1. A package for an integrated circuit chip adapted to operate at microwave frequencies, comprising:

an electrically conductive lead frame having electrical leads extending outwardly from an inner region;

a base section adhesively affixed to a bottom surface portion of the lead frame, portions of the electrical leads extending outwardly from the base section;

a plastic cover; and

wherein the base section and the cover are configured to provide a cavity when the cover and the base section are affixed with the integrated circuit chip being encapsulated within the provided cavity.

2. The package recited in claim 1 wherein the base section comprises a dielectric member.

3. The package recited in claim 2 wherein the base section includes a conductive member affixed to the dielectric member.

4. The package recited in claim 1 wherein the cover has a recess disposed within sidewalls and wherein ends of the sidewalls are affixed to the base section.

5. The package recited in claim 1 wherein the cover is configured to increase surface tension with an adhesive disposed between the cover and the lead frame.

6. The package recited in claim 5 wherein the cover is configured with a ridge to increase the surface tension.

7. The package recited in claim 1 wherein the base section is configured to increase surface tension with an

adhesive disposed between the base section and the lead frame.

8. The package recited in claim 7 wherein the base section is configured with a ridge to increase the surface tension.

9. The package recited in claim 6 wherein the adhesive projects towards an interior of the package a distance in the order of 1% of the width of an exterior length of the package.

10. The package recited in claim 8 wherein the adhesive projects towards an interior of the package a distance in the order of 1% of the width of an exterior length of the package.

11. A method for packaging an integrated circuit chip adapted to operate at microwave frequencies, comprising the steps of:

providing a lead frame having: electrical leads extending outwardly from an inner region of the lead frame;

adhesively affixing a base section to the lead frame with portions of the electrical leads extending outwardly from the base;

connecting electrical wires between the integrated circuit chip and the electrical leads; and

affixing a cover to provide the package with such integrated circuit chip being disposed within a cavity formed by affixed cover and base section.

12. A method for packaging an integrated circuit chip adapted to operate at microwave frequencies, comprising the steps of:

providing a lead frame having a plurality of sites therein, each site having: electrical leads extending outwardly from an inner region of the site;

adhesively affixing each one of a plurality of plastic base section over a corresponding one of the site;

connecting electrical wires between the integrated circuit chip at each one of the plurality of sites and the electrical leads at the corresponding one of the sites; and

adhesively affixing covers to encapsulate each one of the integrated circuits and the electrical wires connected thereto within a cavity formed by the corresponding one of the plurality of the affixed corresponding one of the covers.

13. A package for an integrated circuit chip adapted to operate at microwave frequencies, comprising:

an electrically conductive lead frame having electrical leads adapted for electrical connection to the integrated circuit;

a base section having;

a dielectric member;

a conductive member;

wherein the dielectric member has an aperture disposed in registration with an inner region of the lead frame and the conductive member has one upper portion thereof adhesively affixed to a bottom potion of the dielectric member and another upper portion electrically connected to a bottom ground plane conductor of the

integrated circuit, such integrated circuit chip being disposed in registration with the aperture;

wherein the dielectric member is disposed between the lead frame and the conductive member;

a plastic cover; and

wherein the base section and the cover are configured to provide a cavity when the cover and the base section are affixed with the integrated circuit chip being disposed with such provided cavity and with a bottom surface portion of the conductive member being exposed exteriorly of the package.

14. A method for packaging an integrated circuit chip adapted to operate at microwave frequencies, comprising the steps of:

providing a lead frame having electrical leads extending outwardly from an interior region of the lead frame;

electrically connecting a conductive member of a base section to a bottom ground plane conductor of the integrated circuit with an apertured dielectric disposed between the lead frame and the conductive member and the aperture in registration with the integrated circuit chip;

connecting electrical wires between the integrated circuit chip and the electrical leads;

affixing a plastic cover to provide a package for the integrated circuit chip with such integrated circuit chip being disposed within a cavity formed by the affixed cover and with a portion of the electrically conductive member being exposed exteriorly of the package.

15. A method for packaging an integrated circuit chip adapted to operate at microwave frequencies, comprising the steps of:

providing a lead frame having electrical leads extending outwardly from an inner region of the lead frame and a dielectric member of a base section, such dielectric member having an aperture;

connecting electrical wires between the integrated circuit chip and the electrical leads and a bottom ground plane conductor of the integrated circuit chip to an electrically conductive member of the base section with the integrated circuit chip being disposed in registration with the aperture;

adhesively affixing the base section and a cover to provide a package for the integrated circuit chip with such integrated circuit chip being disposed within a cavity formed by the base section and the cover and with a portion of the electrically conductive member being exposed exteriorly of the package.

16. A package an integrated circuit chip adapted to operate at microwave frequencies, such package comprising:

a lead frame having electrical leads extending outwardly from an inner region of the lead frame;

a base section having:

a dielectric having an aperture;

an electrically conductive member having un upper surface adapted for electrical connection to a bottom ground plane of the integrated circuit chip when the integrated circuit chip is in registration with the aperture;

a dielectric cover, such cover and base section being configured to provide a cavity for the integrated

circuit chip when the base section and the cover are affixed and to expose a bottom portion of the conductive member exteriorly of the package.

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